

Borrower: BUF
Service Level: Invoices / Default
Delivery: Odyssey

1/31/2007 11:24:50 AM

ILL: 27247574



Patron: Chung, Deborah

ILLiad TN: 269212



Fax: 716-645-6298

Ariel: 128.205.111.1

Odyssey: 128.205.243.243

Charge

Maxcost: \$30.00IFM

Lending String: YDM,*AFU,RLR,CAI,MFM

ISSN: 91502><ODYSSEY;128.2

OCLC: 37691056

ILL - AFU

UNIVERSITY OF ARKANSAS
365 N MCILROY AVE
FAYETTEVILLE AR 72701-4002

RETURN POSTAGE GUARANTEED

LIBRARY MAIL

STATE UNIV NY AT BUFFALO
ASL INTERLIBRARY LOAN
234 LOCKWOOD MEMORIAL LIBRARY
BUFFALO NY 14260-2200

Call #: TK7874 .A38

Location: PERIODICALS ROOM

Volume: 33

Issue: 4

Year:
2006

Pages: 8-11

Journal Title: Advancing microelectronics /

Article Author:

Article Title: D.D.L. Chung; Advances in thermal interface materials

Notice: This material may be protected by Copyright Law (Title 17 U.S. Code)

Date: 1/31/2007 11:24:50 AM

Initials: _____

Shelf: _____ **Per:** _____

Sort: _____ **ILL:** _____

Bad Cite: _____

Years checked _____

Table of Contents / Index _____

Advances in Thermal Interface Materials

D.D.L. Chung, Composite Materials Research Laboratory, University at Buffalo, State University of New York, Buffalo, NY 14260-4400, USA.
E-mail: ddlichung@buffalo.edu.

Overheating is the most critical problem in the computer industry, as it limits the further miniaturization, power, performance and reliability. An important way to alleviate this problem is to improve the thermal contact between the microprocessor and heat sink in the computer [1-5]. For this purpose, a material, known as a thermal interface material [6], is placed at the interface. In the case of a microprocessor with an integrated heat spreader, a thermal interface material is also needed for the interface between the die and the heat spreader.

Thermal interface materials can be in the form of a paste (known as a thermal paste, most commonly based on silicone) [7-13], flexible graphite [14-17], phase change materials [18-20], low melting alloys [21,22] and nanostructured carbon materials [10-13, 23-26]. A thermal paste should conform to the surface topography of the adjoining surfaces, because no surface is perfectly smooth and the valleys in the surface topography trap air, which is a thermal insulator.

Carbon black is a nanostructured carbon that is in the form of porous agglomerates of nanoparticles (size 30 nm [10-13]). Due to this structure, carbon black is highly conformable. In addition, the nanoparticles in carbon black can fill the microscopic valleys in the surface topography of the mating surfaces. Thus, in spite of the moderate thermal conductivity of carbon black, carbon black paste outperforms silver paste and solder as a thermal interface material [11].

The performance of a thermal interface material is enhanced by high conformability, high thermal conductivity and low thickness. For two mating surfaces that are flat and well aligned (i.e., parallel), the thickness of the thermal interface material is ideally such that the interface material is just enough to fill the valleys in the surface topography of the mating surfaces. However, the two surfaces may not be flat, i.e., there may be some curvature in one or both surfaces. Moreover, the two surfaces may not be well aligned, due to the way that the two surfaces are brought together. The more different are the areas of the two surfaces, the greater is the chance of misalignment during fastening. In the case where the surfaces are not flat or not well aligned, the gap between the surfaces can be substantial, at least locally. As a result, the thermal interface material needs to be relatively thick and is referred to as a gap-filling material.

Due to its relatively large thickness, a gap-filling material is commonly in the form of a sheet. An example is "flexible graphite" [14-17], which is a graphite

sheet that is flexible and is resilient in the direction perpendicular to the sheet. The resiliency is made possible by the microstructure, which involves the mechanical interlocking of exfoliated graphite in the absence of a binder [27]. In general, a gap-filling material in the form of a sheet may be made more effective by coating both sides of the sheet with a thermal paste. This paper addresses gap-filling materials, in addition to thermal pastes.

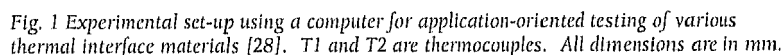
Due to variability in the testing conditions (e.g., roughness and thermal conductivity of the mating surfaces) and methods (e.g., positions relative to the thermal interface of the temperature measurement), the relative performance of various thermal interface materials should be evaluated by using the same testing method and condition. Although the performance of various thermal interface materials has been reported, comparative evaluation has received little attention. Comparative evaluation is necessary for providing guidelines for the choice of a thermal interface material. In addition, it sheds light on the science of thermal interface materials.

Comparative evaluation [28] in this paper uses each of two methods. One method involves application-oriented testing by using a computer and measuring the temperature rise during computer operation. Another method involves scientific testing by using the Guarded Hot Plate Method, which involves measurement of the heat flux in the steady state (ASTM Method D5470) [13]. The latter method is more reliable scientifically, but the former method is commonly used in the electronic industry. This paper also provides comparison of the relative performance results obtained by using these two methods.

The application-oriented testing [28] used an Intel Pentium IV flip chip pin-grid array 2 (FC-PGA2) microprocessor (processor core frequency 1.7 GHz, system bus frequency 400 MHz, L2 cache size 256 Kbytes, core voltage 1.75 V) in a 478-pin package, which was integrated with a heat spreader (area = 960 mm²) made of nickel coated copper with surface roughness 8 μ m (Fig. 1). The pins, which were made of Au/Ni plated Kovar, were inserted in a socket that was made of a fiber-reinforced polymer (resin). The thermal interface material under evaluation was placed at the interface between the heat spreader (30 x 30 mm) and an aluminum heat sink (area of 88 x 64 mm and surface roughness 12-21 μ m).

The maximum temperature difference across the interface between the microprocessor and heat sink

The thermal interface materials evaluated are listed in Table 1 [28], where FG denotes flexible graphite of thickness 0.13 mm and Al denotes aluminum foil (1145) of thickness 0.007 mm. Also evaluated were materials in the form of pastes, namely carbon black (1.25 vol.%) polyethylene-glycol (with 3 vol.% dissolved ethyl cellulose) paste [10,11], commercial "Arctic Silver 5" (polyol ester filled with micronized silver particles, together with smaller quantities of submicron particles of boron nitride, zinc oxide and aluminum oxide, such that all the conductive fillers



together make up 88 wt.% of the paste, from Arctic Silver Inc., Visalia, CA) and commercial "Shin-Etsu X-23-7762" (aluminum particle filled silicone from Shin-Etsu MicroSi, Inc., Phoenix, AZ). Flexible graphite and

continued page 10

continued from page 9

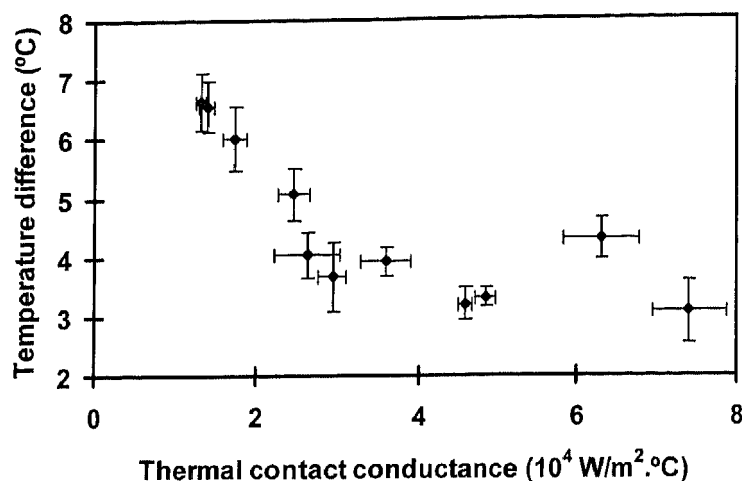


Fig. 2 Temperature difference in computer testing vs. thermal contact conductance in Guarded Hot Plate measurement [28].

aluminum foil that had been coated with each of the pastes on both sides were also included in the comparative study.

Table 1 shows the temperature difference obtained by application-oriented evaluation using the computer [28]. The best materials are carbon black by itself, Shin-Etsu by itself and Al coated with Shin-Etsu; the second best materials are flexible graphite coated with carbon black or Shin-Etsu and Al coated with carbon black; the third best materials are Arctic Silver by itself and aluminum coated with Arctic Silver. The superiority of carbon black and Shin-Etsu over Arctic Silver (each by itself) reflects the alignment and smoothness

of the mating surfaces and the consequent thin gap at the interface. The thin gap favors an interface material that exhibits high conformability.

Table 1 also shows the thermal contact conductance obtained using the Guarded Hot Plate Method [28]. A low value of the temperature difference (based on application-oriented testing using the computer) correlates with a high value of the contact conductance in most cases. The main discrepancy pertains to the results for carbon black by itself and for Arctic Silver by itself. The temperature difference is lower for carbon black by itself than for Arctic Silver by itself, but the contact conductance is lower for carbon black by itself than for Arctic Silver by itself. This discrepancy is attributed to the greater smoothness of the microprocessor package than the copper surfaces used in the contact conductance measurement. The carbon black paste is more fluidic than Arctic Silver, so it has greater conformability, thus performing particularly well for smoother surfaces [10]. The heat sink surface is rough, so conformability to the surface topography of the heat sink can be attained for both carbon black paste and Arctic Silver. However, conformability to the microprocessor surface is attained to a greater degree by the carbon black paste than Arctic Silver.

Fig. 2 shows the extent of correlation between the results of application-oriented computer testing and those of thermal contact conductance measurement. A higher conductance correlates with a lower value of the temperature difference when the conductance is below $3 \times 10^4 \text{ W/m}^2.\text{°C}$ (i.e., the temperature difference is above 4°C). When the conductance is higher, the temperature difference is essentially independent of the conductance. For example, thermal contact conductance measurement shows that Shin-Etsu by itself is more effective than carbon black by itself, but the values of the temperature difference obtained by computer

testing are close for these two cases (Table 1). This behavior is due to the fact that the thermocouples used in the computer testing are separated by not only the thermal interface material, but also the microprocessor package, the substrate and the socket (Fig. 1). The separation makes the measured temperature difference substantial, even though the actual temperature difference across the mating surfaces may be small. Thus, the computer testing method is not suitable for evaluating high-performance thermal interface materials. A true assessment of the effectiveness of a thermal interface material is the measurement of the thermal contact conductance.

In conclusion, comparative evaluation of the relative effectiveness of various thermal interface materials shows that carbon black paste, whether by itself or as a coating on aluminum or flexible graphite, is

Table 1 Temperature difference at 5 min of Pentium IV computer operation and thermal contact conductance for various thermal interface materials [28].

FG = flexible graphite, Al = aluminum.

Thermal interface material	Temperature difference (°C)	Thermal contact conductance ($10^4 \text{ W/m}^2.\text{°C}$)*
Carbon black	3.32 ± 0.16	4.85 ± 0.13
Arctic Silver	4.30 ± 0.39	6.31 ± 0.39
Shin-Etsu	3.07 ± 0.53	7.41 ± 0.47
FG	6.55 ± 0.43	1.40 ± 0.09
FG + carbon black	3.67 ± 0.27	2.93 ± 0.09
FG + Arctic Silver	6.01 ± 0.55	1.74 ± 0.15
FG + Shin-Etsu	4.04 ± 0.58	2.63 ± 0.18
Al	6.63 ± 0.48	1.32 ± 0.06
Al + carbon black	3.92 ± 0.24	3.67 ± 0.31
Al + Arctic Silver	5.06 ± 0.44	2.46 ± 0.18
Al + Shin-Etsu	3.27 ± 0.35	4.59 ± 0.48

* Measured using the Guarded Hot Plate Method, with the thermal interface material between copper surfaces squeezed together at a pressure of 0.46 MPa (50 psi).

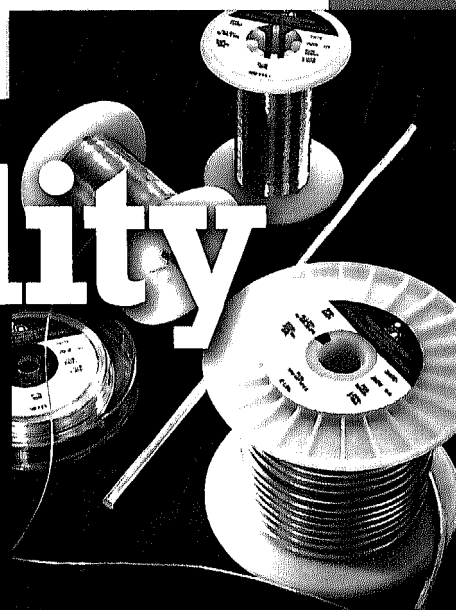
more effective than silver paste (Arctic Silver), but is comparable in effectiveness to aluminum paste (Shin-Etsu). The carbon black paste by itself is as effective as the Shin-Etsu paste coated aluminum. The high effectiveness of the carbon black paste is due to its conformability. The Shin-Etsu paste is more effective than Arctic Silver, whether by itself or as a coating. The relative performance is mostly consistent with that assessed by measuring the thermal contact conductance. The correlation is good for conductance below $3 \times 10^4 \text{ W/m}^2\cdot^\circ\text{C}$. The discrepancy is attributed to the difference in surface roughness between computer and Guarded Hot Plate surfaces.

References

1. E.G. Wolff and D.A. Schneider, *Int. J. Heat Mass Tran.* 41, 3469 (1998).
2. T. Ouellette and M. de Sogno, *Proc. Power Electron. Des. Conf. Power Sources Users Conference*, Cerritos, CA, 134 (1985).
3. M.R. Vogel, *Proc. Int. Intersociety Electron. Packag. Conf. - Advances in Electronic Packaging* (New York, NY: American Society of Mechanical Engineers, 1995), vol. 10-2, p. 989.
4. V. Sartre and M. Lallemand, *Appl. Therm. Eng.* 21, 221 (2001).
5. M. Grujicic, C.L. Zhao and E.C. Dusek, *Appl. Surf. Sci.* 246, 290 (2005).
6. D.D.L. Chung, *J. Mater. Eng. Performance* 10, 56 (2001).
7. L. Maguire, *Microelectron. Reliab.* 45, 711 (2004).
8. M. Grujicic, *Appl. Surf. Sci.* 246, 290 (2005).
9. Y. Xu, X. Luo and D.D.L. Chung, *J. Electron. Packaging* 124, 188 (2002).
10. C.-K. Leong and D.D.L. Chung, *Carbon* 42, 2323 (2004).
11. C.-K. Leong and D.D.L. Chung, *Carbon* 41, 2459 (2003).
12. C.-K. Leong, Y. Aoyagi and D.D.L. Chung, *J. Electron. Mater.* 34, 1336 (2005).
13. C.-K. Leong and D.D.L. Chung, *J. Electron. Mater.* 35(1), 118 (2006).
14. X. Luo, R. Chugh, B. C. Biller, Y. M. Hoi and D.D.L. Chung, *J. Electron. Mater.* 31(5), 535 (2002).
15. M. Smalc, *Int. Electron. Pack. T. Conf. Exhib.* (New York, NY: American Society of Mechanical Engineers, 2003), vol. 2, p. 253.
16. I. Savija, *Int. Electron. Pack. T. Conf. Exhib.* (New York, NY: American Society of Mechanical Engineers, 2003), vol. 2, p. 567.
17. E. Marotta, *IEEE T. Compon. Pack. T.* 28, 102 (2005).
18. Z. Liu and D.D.L. Chung, "Boron Nitride Particle Filled Paraffin Wax as a Phase-Change Thermal Interface Material," *J. Electron. Packaging*, in press.
19. Z. Liu and D.D.L. Chung, *Thermochim. Acta* 366(2), 135 (2001).
20. M.H. Nurawati, *Int. J. Polym. Anal. Ch.* 9, 213 (2004).
21. R. Webb, *Int. Electron. Pack. T. Conf. Exhib.* (New York, NY: American Society of Mechanical Engineers, 2003), vol. 2, p. 537.
22. X. Luo and D.D.L. Chung, *Int. J. Microcircuits Electron. Pack.* 24(2), 141 (2001).
23. Q. Ngo, *Surf. Eng. in Mater. Sci. III* (Warrendale, PA: The Minerals, Metals and Materials Society, 2005) p. 75.
24. K. Zhang, *Electron. Compon. T. Conf.* 1, 60 (2005).
25. T. Lee, *Electron. Compon. T. Conf.* 1, 55 (2005).
26. Y. Wu, *Appl. Phys. Lett.* 87, 213108 (2005).
27. D.D.L. Chung, *J. Mater. Sci.* 22, 4190 (1987).
28. T.A. Howe, C.-K. Leong and D.D.L. Chung, *J. Electron. Mater.*, in press. ♦

Reliability

- Indium solder alloys for package sealing
- High-melting alloys for die-attach



S O L D E R

INDIUM CORPORATION®

www.indium.com
askus@indium.com
PRC +86 (0)512 628 34900
SINGAPORE +65 6268 8678
UK +44 (0) 1908 580400
USA +1 315 853 4900

